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9, six types of **modified partial product** matrices are shown of m by m. Note that m=4 as an example, but m can be 8, or any desired small integer. ...

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containing as rows, **partial products** or **modified partial prod**- ... "reduced length" **modified partial products**. The rows of the ...

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Overlapped multiple-bit scanning multiplication system with banded ...

In a system as recited in claim 2, wherein said assembly means inverts the bits of any **modified partial product** whose sign is negative and appends an ...

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Digital multiplier architecture with triple array summation of ...

A **modified** Booth algorithm minimizes the number of **partial products** generated. Two adder arrays sum the **partial products** in parallel to generate ...

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The proposed system assembles **modified partial products** in a reduced, left edge banded matrix. Such a **partial product** matrix contains rows with encoded ...

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Welcome to IEEE Xplore 2.0: A unified radix-4 **partial product** ...

Modified Booth recoding is generally employed in sequential and parallel multipliers as it reduces the number of **partial products** by roughly one half. ...

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modified carry save compression circuits such as 4: 2 and 5: 5: 2. compressors are used to perform decomposed **partial product**. addition. ...

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Inventor Name	City	State/Country
PIUS NG, FUK HO	PLANO	TEXAS
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